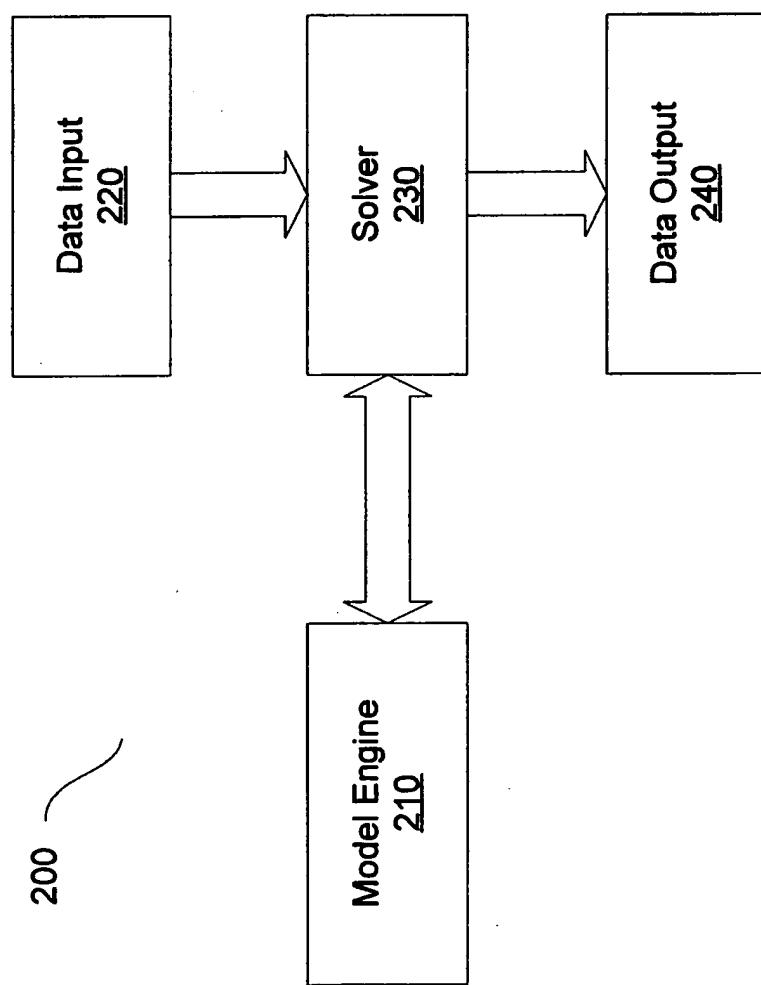


FIG. 1



200

FIG. 2

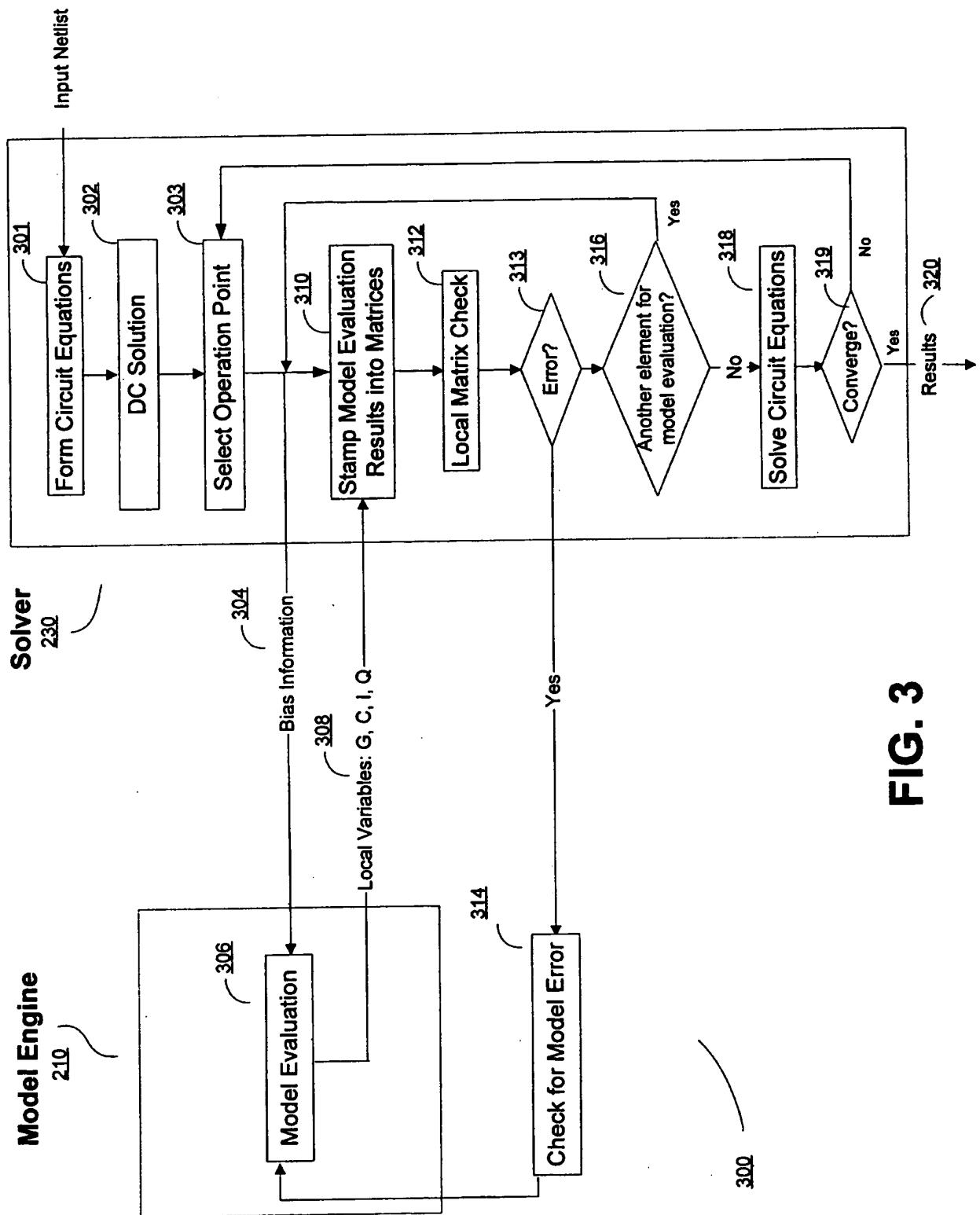


FIG. 3

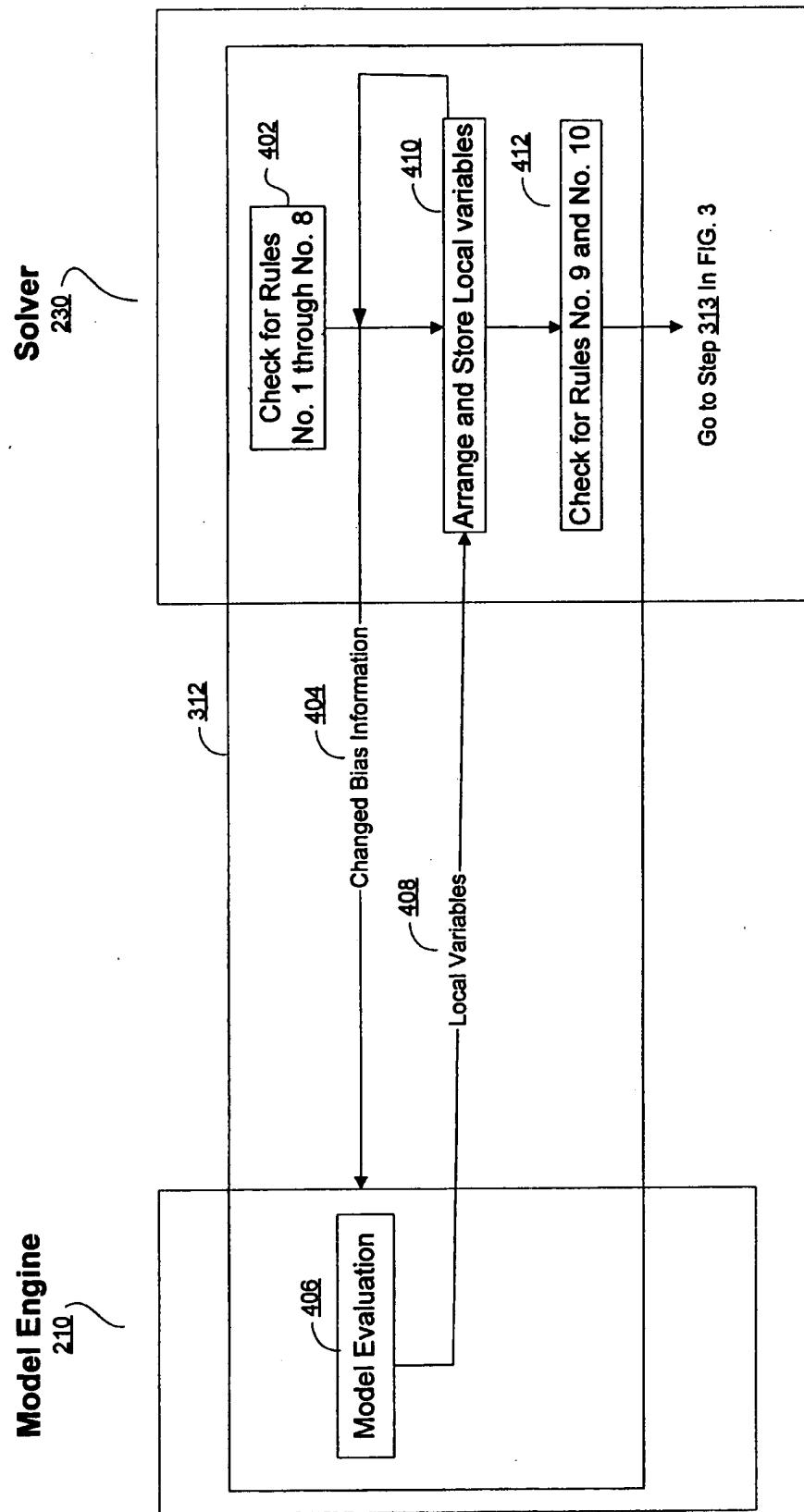


FIG. 4

App No.: Not Yet Assigned
Inventor: Yutao MA et al.

Docket No.: 188122000700

Title: MODEL STAMPING MATRIX CHECK TECHNIQUE IN
CIRCUIT SIMULATOR

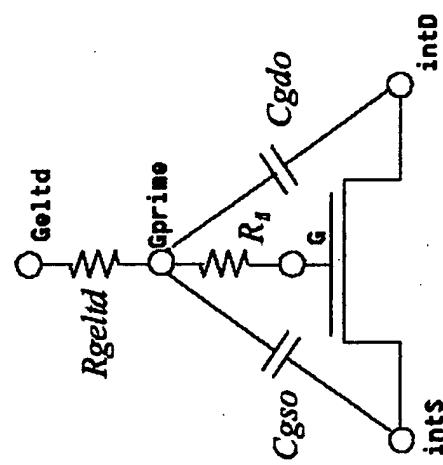


FIG. 5